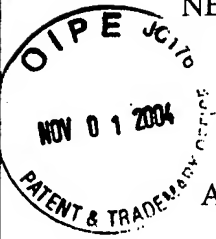


NETL.001DV3

PATENT



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Jayesh R. Bhakta et al.

Group Art Unit 2818

Appl. No. : 10/765,488

Filed : January 27, 2004

For : ARRANGEMENT OF INTEGRATED
CIRCUITS IN A MEMORY MODULE

Examiner : Ly D. Pham

RESPONSE TO AUGUST 23, 2004 OFFICE ACTION

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the August 23, 2004 Office Action, Applicants request the Examiner to reconsider the above-identified patent application in view of the following remarks.

Remarks: Begin on page 2 of this Response.